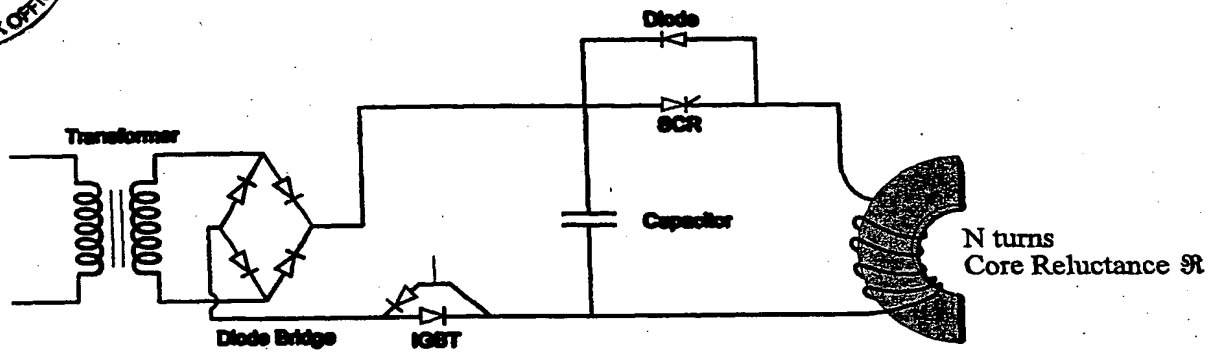
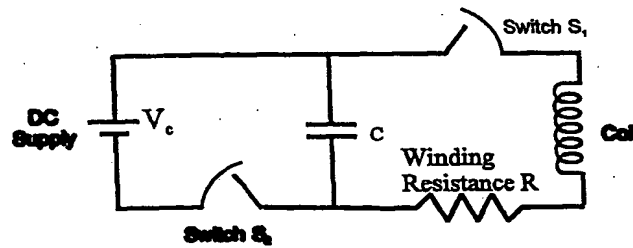




Prior Art



(a) Full Circuit



(b) Simplified equivalent circuit

Fig. 2

Prior Art

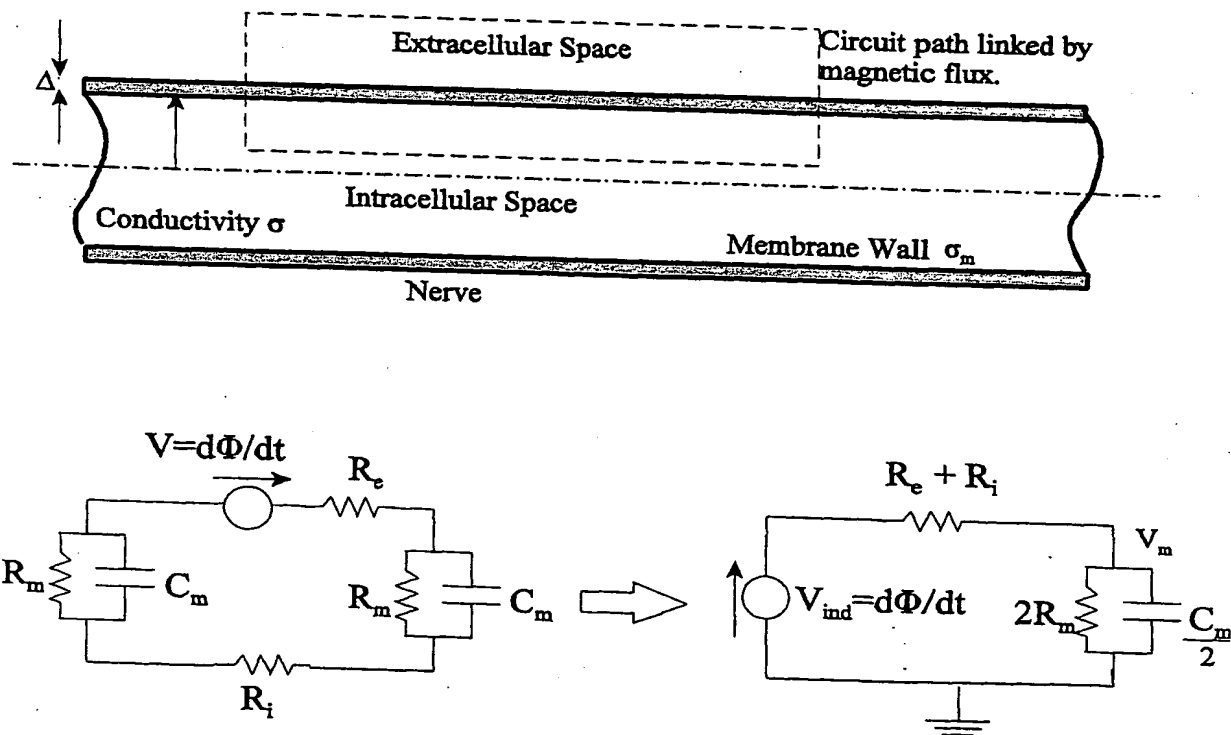


Fig. 3